

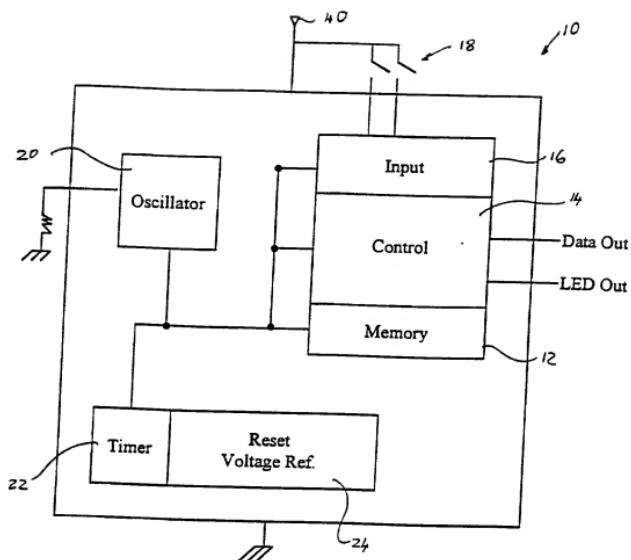
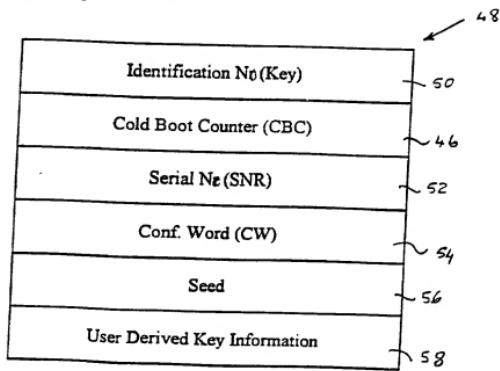
**Fig. 1: Encoder Block Diagram****Fig. 2: Memory Map : Encoder**

Fig. 3: Decoder : Block Diagram

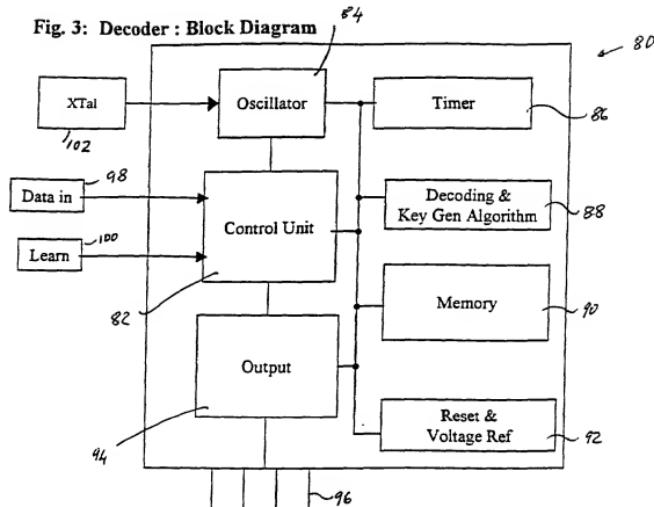
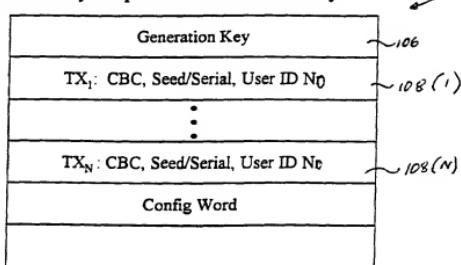


Fig. 4: Decoder Memory Map: Non-volatile memory

Fig 4 a

## Decoder: Volatile Memory Map

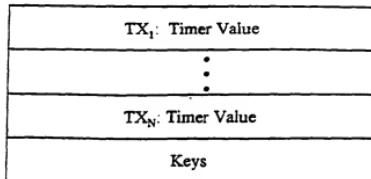
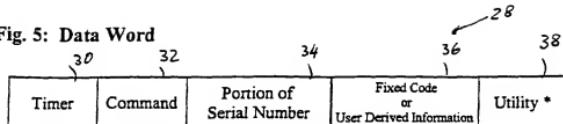
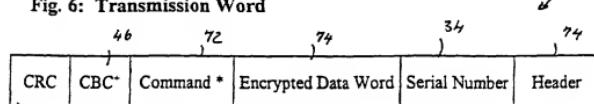


Fig. 5: Data Word



\* repeat, battery level, shift levels

Fig. 6: Transmission Word



\* Info - repeat, battery level, shift levels

+ Only a portion need be sent with every transmission word

Fig. 7: Memory locations for learning encoders.

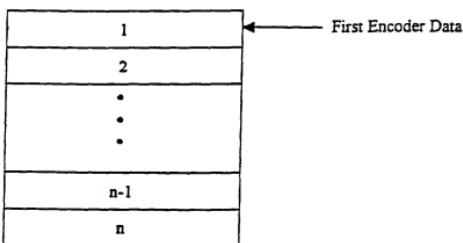


Fig. 8: Learning a second encoder

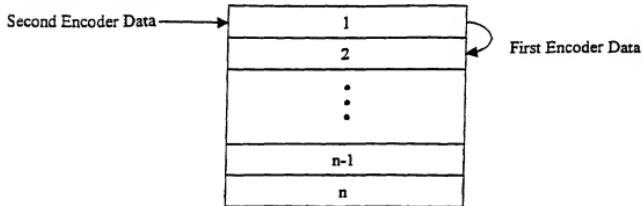
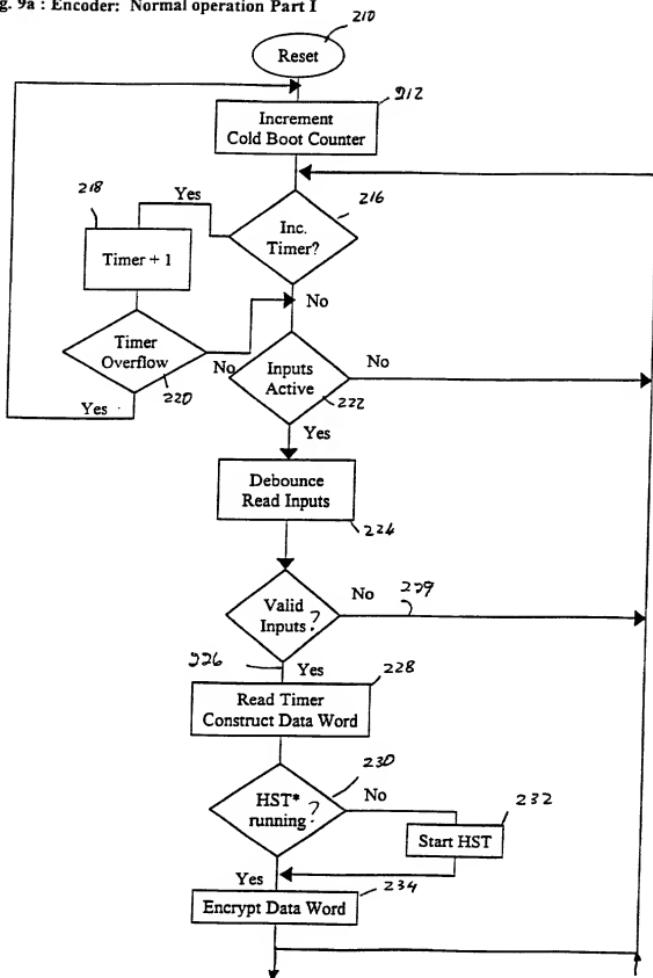
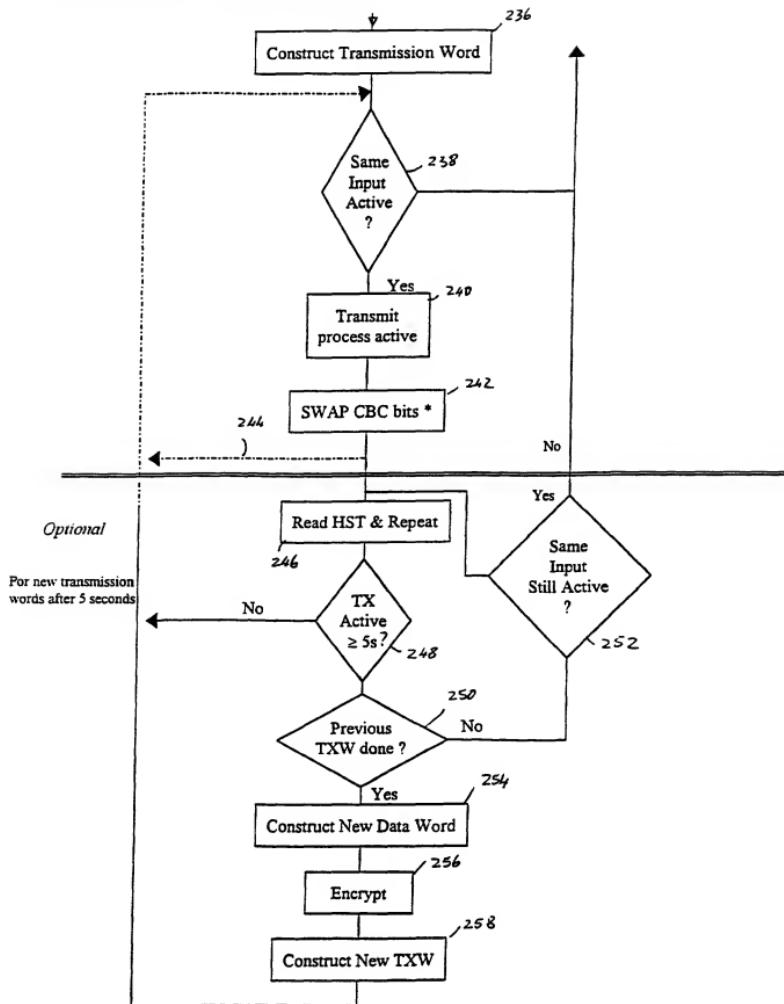


Fig. 9a : Encoder: Normal operation Part I



\* High Speed Timer

Fig. 9b: Encoder: Normal operation Part II



\* with every word in a transmission that consists of a string of words,  
the CBC is partially transmitted

Fig. 10a : Encrypt

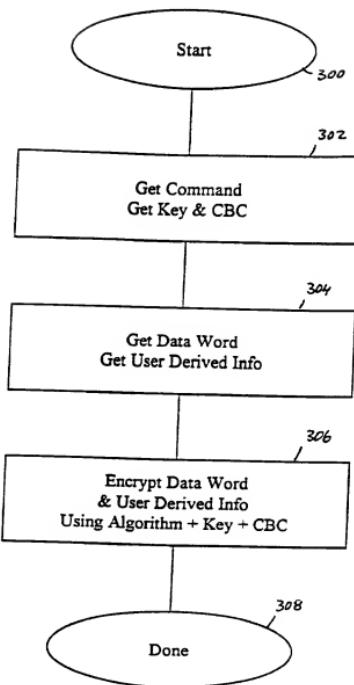
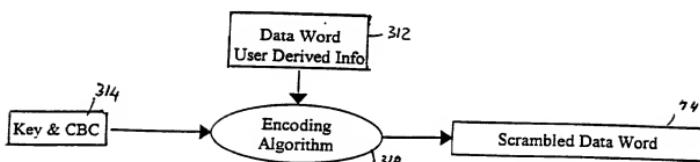


Fig 10b:



**Fig. 11: Decoder : Normal Operation**

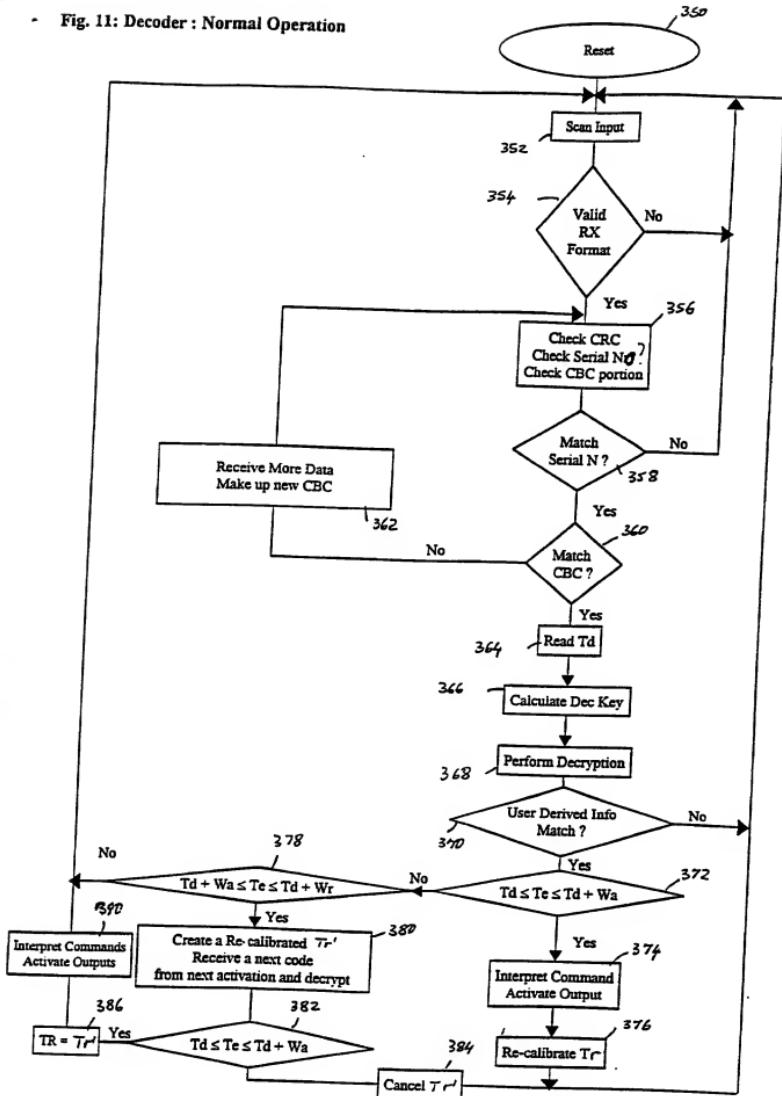
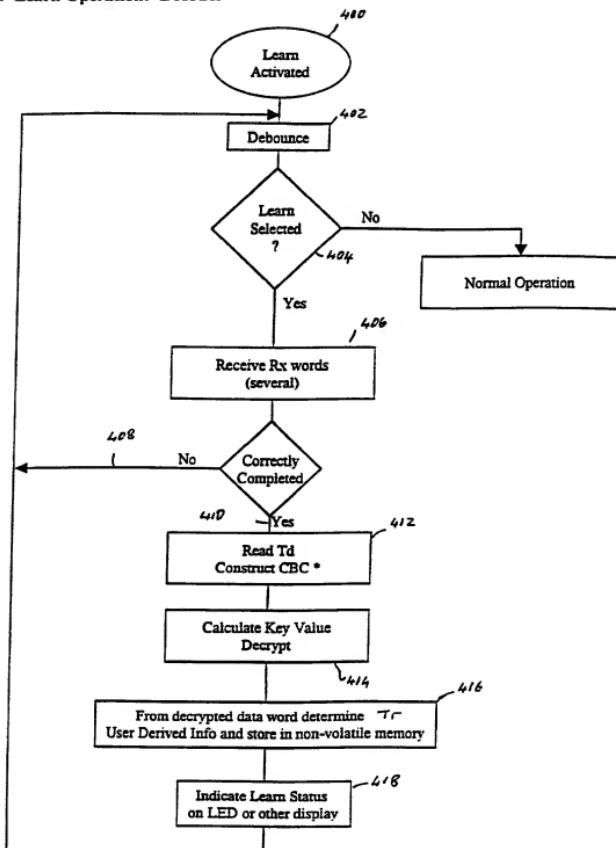
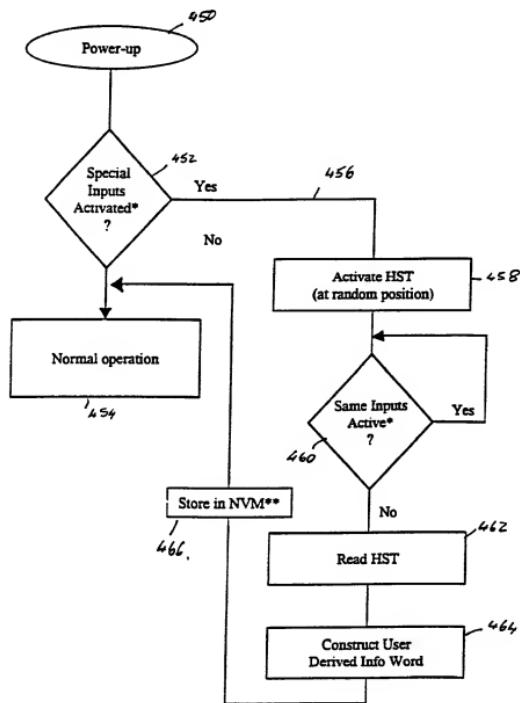


Fig. 12: Learn Operation: Decoder



\* Cold Boot Counter

Fig. 13: Encoder: Setting User Derived Information



\* To activate setting of User Derived Information

\*\* NVM - Non-volatile Memory